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Attorney Docket No. NEKU 20.776 (100806-00242)

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Inventor: Noriyuki KODAMA, et al.  
Serial No.: 10/728,379  
Filed: December 3, 2003  
Title: ELECTRO STATIC DISCHARGE...  
Examiner:  
Art Unit: 2835  
Con. No.: 6385

March 17, 2004

Director of the U.S. Patent and  
Trademark Office  
P.O. Box 1450  
Alexandria, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT**

S I R:

In order to comply with discretionary rules 37 CFR §§1.97 and 1.98, attached hereto is a copy of Form PTO-1449 and a copy of the documents listed thereon. This document contains information in which the Examiner may consider to be important in deciding whether to issue a patent in the instant application.

As this statement is being filed prior to issuance of a first Office Action, no fee is due.

For documents written in a language other than English, English language abstracts are annexed thereto explaining relevancy in accordance with 37 CFR §1.98 (a)(3).

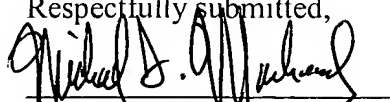
The present Information Disclosure Statement is being submitted in compliance with 37 §CFR 1.56 as an Examiner might consider any cited document important in deciding whether to allow the application to issue as a patent, but the citation of each document is not to be construed as an admission that such document is necessarily relevant or prior art. No representation is

intended that the cited documents represent the results of a complete search, and it is anticipated that the Examiner in the normal course of examination, will make an independent search and will determine the best prior art consistent with 37 CFR 1.104 (a), and in the course of such search will review for relevance every document cited on the attached form even if not initialed.

Early and favorable consideration is respectfully solicited.

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Respectfully submitted,



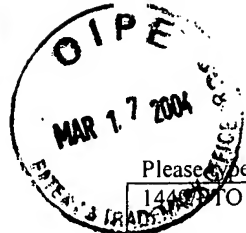
Michael I. Markowitz  
Reg. No. 30,659

**CUSTOMER NO.: 026304**

(Tel) 212-940-8800

(Fax) 212-940-8986

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1448 PTO

U.S. Department of Commerce  
Patent and Trademark Office

Application No. : 10/687,203

Filing Date : October 16, 2003

First Named Inventor: M. YOSHIDA

Group Art Unit :

Examiner Name :

Attorney Docket No. : FUSA 20.686

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

Sheet 1 of 1

**U.S. PATENT DOCUMENTS**

Examiner Initials	Cite No. <sup>1</sup>	U.S. Patent Document	Kind Code if known <sup>2</sup>	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns Lines Where Relevant Passages or Relevant Figures Appear
		5,225,702		CHATTERJEE	07/06/1993	
		5,465,189		POLGREEN, et al.	11/07/1995	
		5,502,317		DUVVURY	03/26/1996	
		2003/0075726		KER, et al.	04/24/2003	
		5,012,317		ROUNTRE	04/30/1991	
		4,939,616		ROUNTRE	07/03/1990	
		2002/0153571		MERGENS, et al.	10/24/2002	
		2002/0130366		MORISHITA	09/19/2002	

**FOREIGN DOCUMENTS**

Examiner Initials	Cite No. <sup>1</sup>	Foreign Patent Document Office <sup>3</sup> Number <sup>4</sup> Kind Code <sup>5</sup> (if known)	Country	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YY/Y	Pages, Columns Lines Where Relevant Passages or Relevant Figures Appear
		09-107074	JP	TEXAS INSTR INC <TI>	04/22/1997	
		2003-203985	JP	NEC ELECTRONICS CORP	07/18/2003	

**Other Prior Art-Non Patent Literature Documents**

Examiner Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), data, page(s), volume-issue number(s), publisher, country, where published, source.	Applicant check here if English language translation attached
		AMERASEKERA, et al., "SUBSTRATE TRIGGERING AND SALICIDE EFFECTS ON ESD PERFORMANCE AND PROTECTION CIRCUIT DESIGN IN DEEP SUBMICRON CMOS PROCESSES" 1995 IEEE, pp. 547-549, 0-7803-2700-4	
		MERGENS, et al., "HIGH HOLDING CURRENT SCRS (HHI-SCR) FOR ESD PROTECTION AND LATCH-UP IMMUNE IC OPERATION" 2002 EOS/ESD SYMPOSIUM, pp. 1-8	
		CHATTERJEE, et al., "A LOW-VOLTAGE TRIGGERING SCR FOR ON-CHIP ESD PROTECTION AT OUTPUT AND INPUT PADS" IEEE ELECTRON DEVICE LETTERS, VOL. 12, NO. 1, JANUARY 1991 IEEE pp. 21-22	
Examiner Signature		Date Considered	

Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw a line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup>Unique citation designation number. <sup>2</sup>See attached Kinds of U.S. Patent Documents. <sup>3</sup>Enter Office that Issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup>For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup>Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.1\* if possible. \* Applicant is to place a check mark here if English language Translation is attached.

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